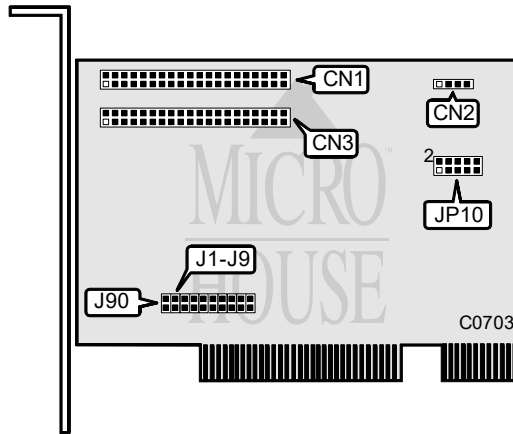


# TYAN COMPUTER CORPORATION S1362 (REV. 1.0)

Q1/95

Data bus: PCI local-bus  
 Size: Half-length, half-height card  
 Hard drive supported: Four IDE (AT) interface drives  
 Floppy drives supported: None



CONNECTIONS	
Function	Location
40-pin IDE (AT) Interface connector	CN1
4-pin connector - drive active LED	CN2
40-pin IDE (AT) Interface connector	CN3
10-pin connector - PCI Legacy IRQ Header	JP10

USER CONFIGURABLE SETTINGS		
Function	Location	Setting
<input type="checkbox"/> Factory configured - do not alter	J1	N/A
<input type="checkbox"/> IRQ polarity meets PCI specifications	J2	closed
<input type="checkbox"/> IRQ polarity does not meet PCI specifications	J2	open
<input type="checkbox"/> On-board IDE channel enabled	J3	open
<input type="checkbox"/> On-board IDE channel disabled	J3	closed
<input type="checkbox"/> Factory configured - do not alter	J90	N/A

PCI BUS IRQ SOURCE		
IRQ Source	J4	J5
<input type="checkbox"/> Primary IDE IRQ	open	closed
<input type="checkbox"/> Secondary IDE IRQ	closed	open

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PCI BUS IRQ				
Setting	J6	J7	J8	J9
IDE IRQ connected to PCI bus IRQ A	closed	open	open	open
IDE IRQ connected to PCI bus IRQ B	open	closed	open	open
IDE IRQ connected to PCI bus IRQ C	open	open	closed	open
IDE IRQ connected to PCI bus IRQ D	open	open	open	closed

MISCELLANEOUS TECHNICAL NOTES
JP10 is used to configure the card to comply with the PCI IDE Legacy convention. Pin 4 of JP10 connects to IRQ14 of the Legacy IRQ card to enable IRQ14. Pin 5 of JP10 connects to IRQ15 of the Legacy IRQ Card to enable IRQ15.