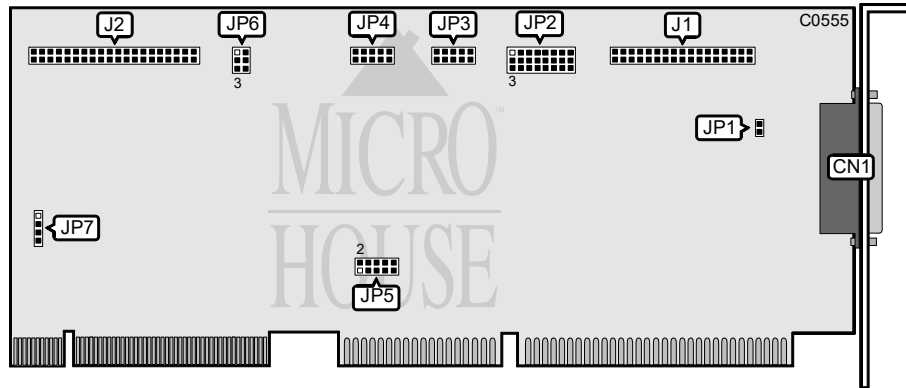


# INFORMTECH INTERNATIONAL, INC.

## IT-PACK4VL VER.1.0

Q3/94

Data bus: 32-bit, VL-bus  
 Size: Three/quarter-length, full-height card  
 Hard drive supported: Two IDE (AT) Interface drives  
 Floppy drives supported: Two 360KB, 720KB, 1.2MB or 1.44MB drives



CONNECTIONS	
Function	Location
34-pin control cable connector - floppy drive	J1
40-pin IDE(AT) Interface connector	J2
10-pin serial port 1 - internal	JP3
10-pin serial port 2 - internal	JP4
4-pin connector - drive active LED	JP7
25-pin parallel port - external	CN1

USER CONFIGURABLE SETTINGS		
Function	Location	Setting
Parallel port is bi-directional	JP1	Open
Parallel port is output only	JP1	Closed
Floppy controller enabled	JP2	pins 19 & 20 closed
Floppy controller disabled	JP2	pins 20 & 21 closed

SERIAL PORT 1 CONFIGURATION	
COM/Address	JP2
COM1/3F8h	pins 1 & 2, 5 & 6 closed
COM3/3E8h	pins 2 & 3, 4 & 5 closed
Disabled	pins 2 & 3, 5 & 6 closed

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SERIAL PORT 2 CONFIGURATION	
COM/Address	JP2
COM2/2F8h	pins 7 & 8, 11 & 12 closed
COM4/2E8h	pins 8 & 9, 10 & 11 closed
Disabled	pins 8 & 9, 11 & 12 closed

PARALLEL PORT CONFIGURATION	
LPT/Address/IRQ	JP2
LPT1/378h/IRQ7	pins 13 & 14, 17 & 18, 23 & 24 closed
LPT2/278h/IRQ5	pins 14 & 15, 16 & 17, 22 & 23 closed
LPT3/3BCh/IRQ7	pins 13 & 14, 16 & 17, 23 & 24 closed
Disabled	pins 14 & 15, 17 & 18 closed

HARD DRIVE SPEED	
Speed	JP6
Speed 1	pins 2 & 3, 4 & 5 closed
Speed 0	pins 1 & 2, 4 & 5 closed
Speed 2	pins 2 & 3, 5 & 6 closed

EXTERNAL BIOS	
Address	JP5
D8000h	pins 5 & 6, 7 & 8, 9 & 10 closed
DE000h	pins 9 & 10 closed
DC000h	pins 7 & 8, 9 & 10 closed
DA000h	pins 5 & 6, 9 & 10 closed
D8000h	pins 5 & 6, 7 & 8, 9 & 10 closed
D6000h	pins 3 & 4, 9 & 10 closed
D4000h	pins 3 & 4, 7 & 8, 9 & 10 closed
D2000h	pins 3 & 4, 5 & 6, 9 & 10 closed
D0000h	pins 3 & 4, 5 & 6, 7 & 8, 9 & 10 closed
CE000h	pins 1 & 2, 9 & 10 closed
CC000h	pins 1 & 2, 7 & 8, 9 & 10 closed
CA000h	pins 1 & 2, 5 & 6, 9 & 10 closed
C8000h	pins 1 & 2, 5 & 6, 7 & 8, 9 & 10 closed
Disabled	all pins open

Note: JP5 should be left in the default setting unless an address conflict exists.