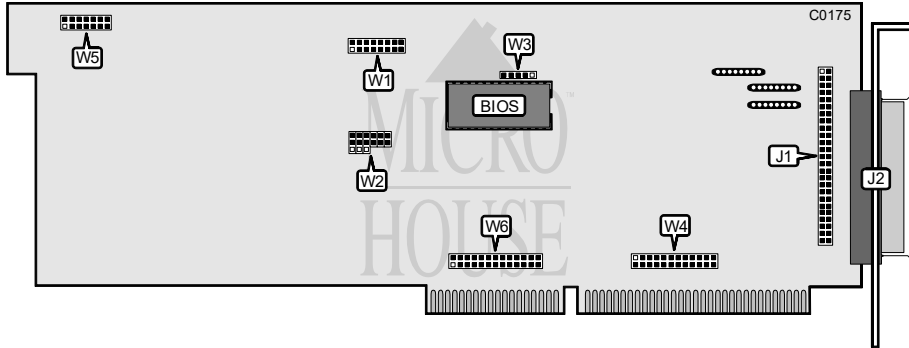


# RANCHO TECHNOLOGY, INC. RT10-AT

Q3/92

Data bus: 16-bit, ISA  
 Size: Full-length, full-height card  
 Hard drives supported: Up to seven SCSI devices  
 Floppy drives supported: None



CONNECTIONS	
Function	Location
50-pin SCSI connector-internal	J1
50-pin SCSI connector-external	J2

I/O PORT ADDRESS									
Address	W1/1	W1/2	W1/3	W1/4	W1/5	W1/6	W1/7	W1/8*	
i 340h	closed	closed	closed	open	closed	open	open	closed	
348h	open	closed	closed	open	closed	open	open	closed	
350h	closed	open	closed	open	closed	open	open	closed	
358h	open	open	closed	open	closed	open	open	closed	
360h	closed	closed	open	open	closed	open	open	closed	
Note	* Open disables Port Address								

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**RANCHO TECHNOLOGY, INC.**  
**RT10-AT**

... continued from previous page

BIOS ADDRESS						
Address	W2/1	W2/2	W2/3	W2/4	W2/5	W2/6*
D000h	pins 1 & 2	pins 2 & 3	pins 2 & 3	closed	open	closed
D400h	pins 1 & 2	pins 2 & 3	pins 1 & 2	closed	open	closed
D800h	pins 1 & 2	pins 2 & 3	pins 2 & 3	open	open	closed
DC00h	pins 1 & 2	pins 2 & 3	pins 1 & 2	open	open	closed
C000h	pins 1 & 2	pins 2 & 3	pins 2 & 3	closed	closed	closed
C400h	pins 1 & 2	pins 2 & 3	pins 1 & 2	closed	closed	closed
C800h	pins 1 & 2	pins 2 & 3	pins 2 & 3	open	closed	closed
CC00h	pins 1 & 2	pins 2 & 3	pins 1 & 2	open	closed	closed
Note: Pins designated should be in the closed position. * Open disables BIOS Address						

INTERRUPT REQUEST (BUS MASTER)					
IRQ	W6/9	W6/10	W6/11	W6/12	W6/13
IRQ15	closed	open	open	open	open
IRQ14	open	closed	open	open	open
IRQ12	open	open	closed	open	open
IRQ11	open	open	open	closed	open
IRQ10	open	open	open	open	closed

ROM SIZE		
ROM Size	W3/pins 2 & 3	W3/pins 4 & 5
BIOS ROM size is 8KB	open	open
BIOS ROM size is 16KB	closed	open
BIOS ROM size is 32KB	closed	closed

DMA CHANNEL (HOST)			
Channel	W4/jumpers 7 & 10	W4/jumpers 8 & 11	W4/jumpers 9 & 12
1	closed	open	open
2	open	closed	open
3	open	open	closed

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**RANCHO TECHNOLOGY, INC.**  
**RT10-AT**

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INTERRUPT REQUEST (HOST)						
IRQ	W4/1	W4/2	W4/3	W4/4	W4/5	W4/6
IRQ3	closed	open	open	open	open	open
IRQ4	open	closed	open	open	open	open
IRQ5	open	open	closed	open	open	open
IRQ6	open	open	open	closed	open	open
IRQ7	open	open	open	open	closed	open
IRQ9	open	open	open	open	open	closed

READ PORT CONFIGURATION JUMPERS - JP5						
Setting	D0	D1	D2	D3	D4	D5
Address is 340h	open	open	N/A	N/A	N/A	N/A
Address is 328h	closed	open	N/A	N/A	N/A	N/A
Address is 310h	open	closed	N/A	N/A	N/A	N/A
Address is 300h	closed	closed	N/A	N/A	N/A	N/A
RAM address. is 0:4B0h	N/A	N/A	open	N/A	N/A	N/A
RAM address. is 0:300h	N/A	N/A	closed	N/A	N/A	N/A
Data transfer polled I/O	N/A	N/A	N/A	open	open	N/A
DREQ5/DACK5 (HS)	N/A	N/A	N/A	closed	open	N/A
DREQ6/DACK6 (HS)	N/A	N/A	N/A	open	closed	N/A
DREQ3/DACK3	N/A	N/A	N/A	closed	closed	N/A
Parity, Inquiry & Verify Cmd. enabled	N/A	N/A	N/A	N/A	N/A	open
Parity, Inquiry & Verify Cmd. disabled	N/A	N/A	N/A	N/A	N/A	closed

DMA CHANNEL (BUS MASTER) JUMPERS - W6				
Channel	Jumpers 1 & 5	Jumpers 2 & 6	Jumpers 3 & 7	Jumpers 4 & 8
7	closed	open	open	open
6	open	closed	open	open
5	open	open	closed	open
0	open	open	open	closed

