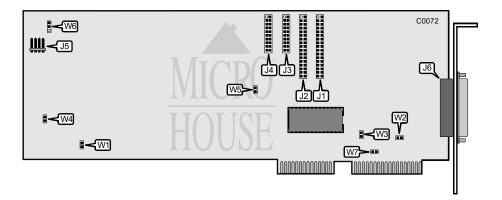
Q4/92

DATA TECHNOLOGY CORPORATION DTC5287

16-bit, ISA Data bus:

Two RLL ST506 drives
Two 360KB, 720KB, 1.2MB, or 1.44MB Size: Hard drives supported: Floppy drives supported:

Sector interleave:



CONNECTIONS		
Function	Location	
34-pin data cable connector-floppy drive	J1	
34-pin control cable connector-hard drive	J2	
20-pin data cable connector-drive 0	J3	
20-pin data cable connector-drive 1	J4	
4-pin connector-drive active LED	J5	
DB-37 control cable connector-external floppy drive	J6	

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USER CONFIGURABLE SETTINGS		
Function	Location	Setting
í BIOS address is C800h	W2	open
BIOS address is D800h	W2	closed
í BIOS enabled	W3	closed
BIOS disabled	W3	open
í Factory configured - do not alter	W5	closed
í Factory configured - do not alter	W6	pins 2 & 3 closed
í BIOS ROM chip is 2764	W7	open
BIOS ROM chip is 27128	W7	closed

HARD DRIVE PORT ADDRESS		
Address	W1	W4
í Port addresses are 1F0-1F7 3F2-3F7h	open	open
Port addresses are 170-177 372-377h	closed	closed

MISCELLANEOUS TECHNICAL NOTES

This controller has a built-in low-level format program. To enter the program run the DEBUG utility supplied with DOS and at the prompt enter G=C800:5.x

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