VLSI TECHNOLOGY, INC.

VL82CPCAT

12 MHz AND 16 MHz PC/AT-COMPATIBLE SYSTEM CHIP SETS

FEATURES

- Fully compatible with IBM PC/AT-type designs
- High-integration five-chip set
- Reduces non-memory system device count from 110 to 16
- Devices are available as "cores" for user-specific designs
- All devices designed in CMOS for low power consumption
- Supports 12 MHz processor clock
- 16 MHz version of chip set with page mode memory access will be available in 1988

DESCRIPTION

The IBM PC/AT compatible chip set from VLSI Technology, Inc. supports 1-Megabit dynamic RAMs, and is utilized in systems with clock speeds up to 12 MHz. The chip set provides the IBM PC/AT compatible system with a completely compatible low-cost board design solution. Further, since the devices were designed using VLSI's design tools, the devices can be quickly modified for use as cores in user-specific designs.

The five device chip set has been designed using the highest integration consistent with economic and reliable system design. The VL82C103 Address Buffer and VL82C104 Data Buffer are offered in separate packages, although their circuit is relatively small. If they were offered as a single device, the pin count would be extremely high, or some performance degradation would occur.

The devices are manufactured with VLSI's advanced high-performance CMOS process and all five are available in a JEDEC-standard 84-pin plastic leaded chip carrier (PLCC) package.

BLOCK DIAGRAM

MOTHERBOARD BLOCK DIAGRAM
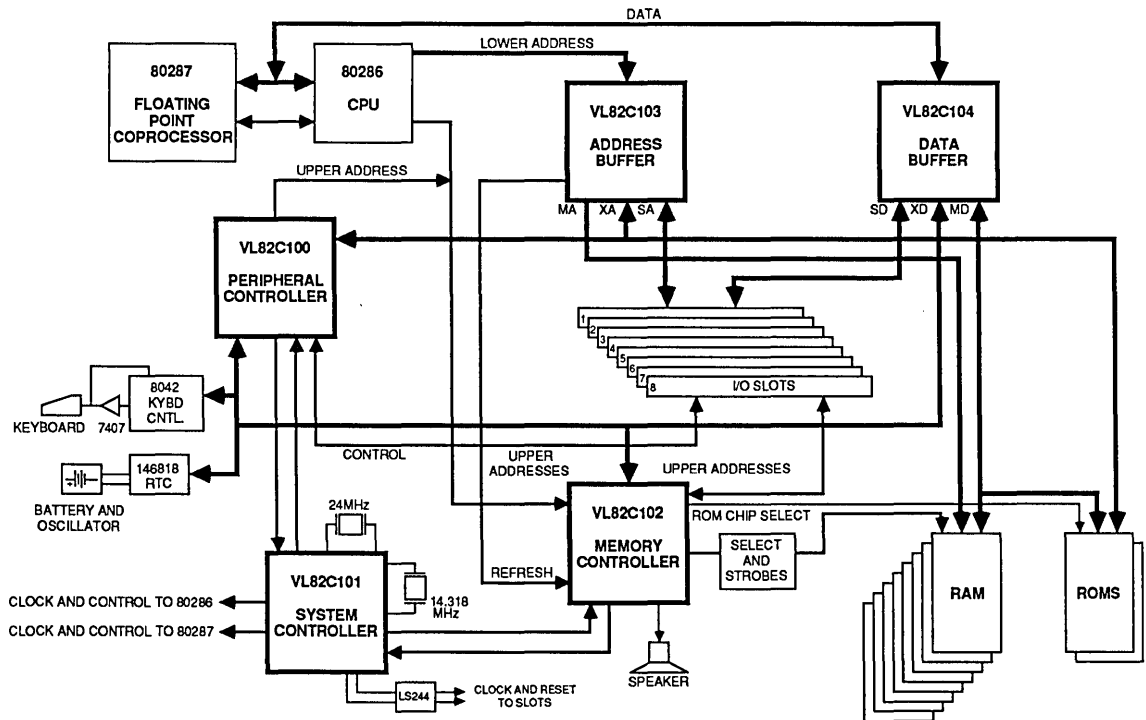
PLEASE CONSULT THE PC/AT-COMPATIBLE CHIP SET USERS MANUAL FOR DETAILED INFORMATION: VL82CPCAT (12 MHz) VL82CPCPM (16 MHz)

ORDER INFORMATION

Part Number	Clock Frequency	Package
VL82CPCAT-QC	12 MHz	(5 Chips) Plastic Leaded Chip Carrier (PLCC)
VL82CPCPM-QC	16 MHz	(6 Chips) Plastic Leaded Chip Carrier (PLCC)

Note: Operating temperature range is 0°C to +70°C