

There are four kinds of C55:

C55 for 650i Ultra

FSB 1066/800MTps

DDR2-800/667/533, up to 32GB

18 PCI Express lanes with links 16,1,1

C55 for 650i SLI (printed "NF650I-SLI-N-xx" or "C55-SLIX8N-xx" on the chipset surface)

FSB 1066/800MTps

DDR2-800/667/533, up to 32GB

18 PCI Express lanes with links 16,1,1 or 8,8,1,1

C55 for 680i SLI "Lite" (printed "C55-SLIX8N-xx" on the chipset surface)

FSB 1333/1066/800MTps

DDR2-1200*/800/667/533 (* for SLI-ready memory with EPP), ECC or Non-ECC, up to 32GB

18 PCI Express lanes with links 16,1,1

C55 for 680i SLI (printed "NF680I-SLI-N-xx" or "C55-SLIX16N-xx" on the chipset surface)

FSB 1333/1066/800MTps

DDR2-1200*/800/667/533 (* for SLI-ready memory with EPP), ECC or Non-ECC, up to 32GB

26 PCI Express lanes with links 16,8,1,1

Actually there are only two kinds of C55, however:

C55

FSB 1333/1066/800MTps

DDR2-1200*/800/667/533 (* for SLI-ready memory with EPP), ECC or Non-ECC, up to 32GB

18 PCI Express lanes with links 16,1,1 or 8,8,1,1

C55XE

FSB 1333/1066/800MTps

DDR2-1200*/800/667/533 (* for SLI-ready memory with EPP), ECC or Non-ECC, up to 32GB

26 PCI Express lanes with links 16,8,1,1

The only difference between C55 and C55XE is that C55XE has an extra x8 PCIe link (for a third PEG port which will be used by a future nVIDIA solutions for particle).

C55 for 650i Ultra/650i SLI/680i SLI "Lite" are all the same as the C55 vanilla (identified as 650i SLI SPP by CPU-Z) with some functions disabled at the motherboard/BIOS level. C55 for 680i is the C55XE (identified as 680i SLI SPP or erroneously C60 by CPU-Z; C60 is the canceled IGP solution for Intel)