

# **FE2100 PC BUS FLOPPY DISK CONTROLLER INTEGRATED CIRCUIT**

- **100% Hardware & Software compatible with the PC BUS**
  - **TTL compatible**
  - **Integral Crystal Oscillator Circuit Precompensation**
  - **48 Pin Dip**
  - **Variable Write Precompensation**
  - **Digital Data Separator**
  - **Track Selectable Write**
  - **HCMOS Technology**
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## **Product Overview**

The Faraday Floppy Disk Controller (FE2100) is a CMOS integrated circuit designed to complement the NEC 765 or Intel 8272 type of Floppy Disk Controller Chip to provide a complete PC BUS compatible Floppy Disk Adapter. The FE2100 operates from a +5 volts supply and simply requires that an 8 Mhz crystal or TTL-level clock be connected to X1 pin. All inputs and outputs are TTL compatible.

The FE2100 has been designed for OEMs who would like to reduce cost, lower power requirements, increase reliability, and reduce board size over that of a functionally equivalent IBM PC or XT floppy disk controller. The FE2100 replaces a total of 18 components.

The FE2100 is a 3.5 micron CMOS gate-array packaged in an 48 Pin Dip.

## FE2100 Chip Replacement Chart

74LS244	74LS161
74LS174	74LS175 (4)
74S288	74LS139
74LS153	74LS393
74LS74	74LS32
74LS08 (2)	74LS04 (2)
74LS00	

No. of chips replaced = 18